



Your PDF Guides

You can read the recommendations in the user guide, the technical guide or the installation guide for KINGSTON KHX1600C9D3B1-2G. You'll find the answers to all your questions on the KINGSTON KHX1600C9D3B1-2G in the user manual (information, specifications, safety advice, size, accessories, etc.). Detailed instructions for use are in the User's Guide.

User manual KINGSTON KHX1600C9D3B1-2G
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Memory Module Specification

KHX1600C9D3B1/2G
2GB 256M x 64-Bit DDR3-1600MHz
CL9 240-Pin DIMM

DESCRIPTION:
This document describes Kingston's 256M x 64-bit 2GB (2048MB) DDR3-1600MHz CL9 SDRAM (Synchronous DRAM) memory module, based on sixteen 128M x 8-bit DDR3 FBGA components. This module has been tested to run at DDR3-1600MHz at a low latency timing of 9-9-9-27 at 1.7V to 1.9V. The SPD is programmed to JEDEC standard latency DDR3-1333MHz timing of 9-9-9 at 1.5V. This 240-pin DIMM uses gold contact fingers and requires +1.5V. The JEDEC standard electrical and mechanical specifications are as follows:

FEATURES:

- JEDEC standard 1.5V ± 0.075V Power Supply
- VDDQ = 1.5V ± 0.075V
- 667MHz I/O for 1333Mbit/sec/pin
- 8 independent internal bank
- Programmable CAS Latency: 5,6,7,8,9,10
- Pseudo CAS
- Programmable Additive Latency: 0, CL - 2, or CL - 1 clock
- Programmable CAS Write Latency (CWL) = 7 (DDR3-1333)
- 8-bit pre-fetch
- Burst Length: 8 (Interleave without any limit, sequential with starting address "000" only), 4 with tCCD = 4 which does not allow seamless read or write (either on the fly using A12 or MRS)
- Bi-directional Differential Data Strobe
- Internal (self) calibration - Internal self calibration through ZQ pin (RZQ: 240 ohm ± 1%)
- On Die Termination using ODT pin
- Average Refresh Period 7.8us at lower than TCASE: 85°C, 3.9us at 85°C < TCASE: 95°C
- Asynchronous Reset
- PCB: Height 1.180" (30.00mm), double sided component



PERFORMANCE:

<input checked="" type="checkbox"/> CL (tDD)	9 cycles
<input checked="" type="checkbox"/> Row Cycle Time (tRCmin)	49.5ns (min)
<input checked="" type="checkbox"/> Refresh to Active/Refresh Command Time (tRFCmin)	110ns
<input checked="" type="checkbox"/> Row Active Time (tRASmin)	36ns (min)
<input checked="" type="checkbox"/> Power	1.800 W (operating)
<input checked="" type="checkbox"/> UL Rating	94 V-0
<input checked="" type="checkbox"/> Operating Temperature	0° C to 85° C
<input checked="" type="checkbox"/> Storage Temperature	-55° C to +100° C



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Manual abstract:

This module has been tested to run at DDR3-1600MHz at a low latency timing of 9-9-9-27 at 1.7V to 1.9V. @@This 240-pin DIMM uses gold contact fingers and requires +1.5V. @@@@4805601-001.A00 54.70 Page 2 .



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