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You can read the recommendations in the user guide, the technical guide or the installation guide for ICOM IC-2200H. You'll find the answers to all your questions on the ICOM IC-2200H in the user manual (information, specifications, safety advice, size, accessories, etc.). Detailed instructions for use are in the User's Guide.

User manual ICOM IC-2200H
User guide ICOM IC-2200H
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Instruction manual ICOM IC-2200H



VHF TRANSCEIVER
IC-2200H

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Manual abstract:

S.A. @@This will ruin the transceiver. DO NOT expose the transceiver to rain, snow or any liquids. @@@@This could damage the transceiver's front end. To upgrade quality, any electrical or mechanical parts and internal circuits are subject to change without notice or obligation. ORDERING PARTS Be sure to include the following four points when ordering replacement parts: 1. 10-digit order numbers 2. Component part number and name 3. Equipment model name and unit name 4.

Quantity required <SAMPLE ORDER> 1130008350 S.IC LV2105V IC-2200H MAIN UNIT Cover 5 pieces 10 pieces 8810009610 Screw FH M2.6x6 ZK IC-2200H REPAIR NOTES 1. Make sure a problem is internal before disassembling the transceiver. 2.

DO NOT open the transceiver until the transceiver is disconnected from its power source. 3. DO NOT force any of the variable components. Turn them slowly and smoothly. 4.

DO NOT short any circuits or electronic parts. An insulated tuning tool MUST be used for all adjustments. 5. DO NOT keep power ON for a long time when the transceiver is defective. 6. DO NOT transmit power into a signal generator or a sweep generator. 7. @@8. @@@@. .

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.....2 - 1 HM-133V ...

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.14 - 2 14 - 4 VOLTAGE DIAGRAM

.14 - 3 SECTION 1 M SPECIFICATIONS GENERAL · Frequency range : Version [EUR] [USA] [EXP], [EXP-1] [EUR-1], [TPE], [KOR] Receive
(MHz) 118.000 174.000*1 118.000 174.
000*2 118.000 174.000*2 144.000 146.000 Transmit (MHz) 144.
000 146.000 144.000 148.000 136.000 174.

000*2 144.000 146.000 *1 Specifications guaranteed 144.000 146.000 MHz only 2 * Specifications guaranteed 144.000 148.000 MHz only Mode
Number of memory channel Usable temperature range Scan type Frequency resolution Frequency stability Power supply requirement Current drain (at 13.8
V DC) · Antenna connector · Dimensions (projections not included) · Weight : FM, AM (Receive only) : 206 (include in 6 band edges memory and 1 call
channel) : 10°C to +60°C; +14°F to +140°F : Full, Program, Priority, Memory channel, Bank, Skip and Tone scans : 5, 10, 12.5, 15, 20, 25, 30 and 50 kHz :
±10 ppm (10°C to +60°C; +14°F to +140°F) : 13.8 V DC ±15 % (negative ground) : Receive Standby (squelched) 0.

8 A Max. audio 1.0 A Transmit at 65 W or 50 W 15.0 A (except [TPE]) at 25 W 9.0 A at 24 W 10.0 A ([TPE] only) at 10 W 6 A (except [TPE]) at 5 W 5 A :
SO-239 (50) : 140(W)×40(H)×146(D) mm; 51/2(W)×19/16(H)×53/4(D) inch : Approx. 1.25 kg; 2 lb 12 oz M TRANSMITTER · Output power : Version
except [TPE], [KOR] [KOR] [TPE] . . . Modulation system : Maximum frequency deviation : Spurious emissions : Microphone connector : High 65 W 50 W
24 W Middle-high 25 W 25 W 10 W Middle-low 10 W 10 W Low 5W 5W 5W Variable reactance frequency ±5.0/±2.5* kHz *except [EXP-1] Less than 60
dBc 8-pin modular (600) M RECEIVER · Receive system · Intermediate frequency Downloaded by RadioAmateur.

EU : Double-conversion superheterodyne : 1st 21.7 MHz 2nd 450 kHz · Sensitivity (at 12 dB SINAD) : Less than 0.18 µV · Squelch sensitivity (threshold) :
Less than 0.13 µV · Selectivity : (Wide) More than ±6 kHz at 6 dB, More than ±14 kHz at 60 dB (Narrow)* More than ±3 kHz at 6 dB, More than ±9 kHz at
55 dB *except [EXP-1] · Spurious and image rejection : More than 60 dB · Audio output power (at 13.8 V) : More than 2.
4 W at 10% distortion with an 8 load · External speaker connector : 2-conductor 3.5(d) mm (1/8")/8 All stated specifications are subject to change without
notice or obligation. 1-1 · UT-115 SPECIFICATIONS M GENERAL : 10°C to +60°C; +14°F to +140°F : 5.0 V DC (4.5 V8.

5 V for AMBE IC VCC line) · AF input level : 1 Vp-p typical · Modulation output level : 350 mVp-p typical · Demodulation input level : 500 mVp-p typical ·
AF output level : 750 mVp-p typical · CODEC type : AMBE · Transfer speed : 4.8 kbps · Control signal input voltage : 3.0 V5.0 V for High 0 V for Low ·
Control signal output voltage : Open collector for High 0 V for Low · Current drain (at 5.0 V DC) : 55 mA typical · Dimensions : 30(W)×38(D) mm;
(projections not included) 13/16(W)×11/2(D) inch · Weight : Approx. 6 g; 7/32 oz (accessories not included) · Connector : AXN430C330 · Usable
temperature range · Power supply requirement Downloaded by RadioAmateur.EU All stated specifications are subject to change without notice or obligation.
1-2 SECTION 2 2-1 IC-2200H · MAIN UNIT (TOP VIEW) INSIDE VIEWS Power switch circuit Q28*: 2SC4684 Q30: DTA143TUA Q31: DTC143ZU
Speaker Power module (IC10: S-AV36) Discriminator (X2: CDB450C24) AF power amplifier (IC9: TA7252AP) VCO circuit 2nd IF IF filter filter F11:
CFWS450F F12: CFWS450HT AGC (Q38: XP6501) Reference oscillator (X1: CR-779 21.

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25MHz) *Located under side of the point · MAIN UNIT (BOTTOM VIEW) 1st Mixer Mixer (Q19: 3SK299) APC controller (IC6: NJM3404AV) RF amplifier (Q27: 3SK299) IF amplifier (Q16: 2SC4406) FM IF IC IF detector (IC4: TA31136FN) D/A converter (IC5: M62363FP) C5V regulator (IC8: TA78L05F) +8V regulator (IC7: TA7808F) TX/RX switch (D6, D7: MA77) PLL IC (IC1: LV2105V) Enphasis switch (Q41: 2SC4617) 2-1 2-2 HM-133V Reset IC (IC3: BD5245G) CPU (IC1: μ PD789071) LED driver Q4, Q5: 2SA1586 DS11*: LT1EP53A LED driver Q6, Q7: 2SA1586 DS10*: LT1EP53A 5V regulator Q1: 2SC2712 D1: MA8056 Regulator switch Q8: 2SC2712 Q9: DTB113ZK EE-PROM (IC6: BR24L02FV IC6: BR24L02FV) [MAIN-A] unit only Data buffer circuit Q2: DTC114EU Q3: DTA114EU *Located under side of the point Downloaded by RadioAmateur.EU 2-2 SECTION 3 OPTIONAL UNIT
INSTALLATION Intall the unit as illustrated below.

Insert tightly to avoid bad contact. Flat cable Remove [DIAL] and unscrew the 2 allen-socket bolts from the front panel using with an allen wrench (2.5 mm; I/I0). Allen-socket bolts Detach the front panel from the main unit. Insulation sheet Return the front panel and the allen-socket bolts to their original position. Attach the insulation sheet (supplied as accessory) to IC on the Front unit. Remove the protective paper attached to the bottom of the optional unit to expose the adhesive strip. NOTE: When attaching the front panel to the main unit, make sure the flat cable are running in the groove to prevent catching between front panel and main unit. 3-1 SECTION 4 CIRCUIT DESCRIPTION The tunable bandpass filters (D13D16) employ varactor diodes to tune the center frequency of the RF passband for wide bandwidth receiving and good image response rejection. These diodes are controlled by the CPU (LOGIC unit; IC1) via the D/A converter (IC5).

4-1 RECEIVER CIRCUITS 4-1-1 ANTENNA SWITCHING CIRCUIT (MAIN UNIT) The antenna switching circuit functions as a low-pass filter while receiving and a resonator circuit while transmitting. The circuit does not allow transmit signals to enter receiver circuits. Received signals enter the antenna connector and pass through the low-pass filter (L47, L48, C208, C210, C217). The filtered signals are passed through the /4 type antenna switching circuit (D16, D19, L45, L46) and limiter circuits (D15). The signals are then applied to the squelch attenuator circuit.

4-1-4 1ST MIXER AND 1ST IF CIRCUITS (MAIN UNIT) The 1st mixer circuit converts the received signals to a fixed frequency of the 1st IF signal with the PLL output frequency. By changing the PLL frequency, only the desired frequency will pass through a pair of crystal filters at the next stage of the 1st mixer. The RF signals from the bandpass filter are applied to the 1st mixer circuit (Q19). The applied signals are mixed with the 1st LO signal coming from the RX-VCO ci components in the AF signals, the squelch circuit switches the AF mute switch. A portion of detected audio signals from the FM IF IC (IC4, pin 9) are applied to the active filter section of the IC (IC4, pin 8) as "SQLIN" signal, after passing through the D/A converter (IC5, pin 12).

The active filter section of the IC amplifies and filters noise components. The filtered signals are applied to the noise detector section in the IC and output from pin 13 as the "NOIS" signal. The "NOIS" signal from IC4 (pin 13) is applied to0, pin 3), and are then output from pin 4. The signals are applied to the ALC amplifier (IC514, pin 3), and are then reapplied to the ALC switch (IC520, pin 8). The signals pass through the high-pass filter (IC3d, pins 12, 14), and are then applied to the analog switch (IC6, pin 4). · IN CASE OF BYPASSING THE ALC CIRCUIT The amplified signals are applied to the ALC switch (IC520, pin 2), and are then output from pin 1. The signals are applied to the ALC switch (IC520, pin 9) again, and then pass through the high-pass filter (IC3d, pins 12, 14) via the IC520, pin 10. The signals are applied to the analog switch (IC6, pin 4). 4-2-5 DRIVE AMPLIFIER CIRCUIT (MAIN UNIT) The drive amplifier circuit amplifies the VCO oscillating signal to the level needed at the power amplifier. The RF signal from the buffer amplifier (Q11) passes through the T/R switch (D6) and is amplified at the pre-drive (Q17) and drive (Q35) amplifiers.

The amplified signal is applied to the power amplifier circuit. 4-2-6 POWER AMPLIFIER CIRCUIT (MAIN UNIT) The power amplifier circuit amplifies the driver signal to an output power level. The RF signal from the drive amplifier (Q35) is applied to the power module (IC10) to obtain 65 W (50 W for Korea version, 25 W for Taiwan version) of RF power. The amplified signals is passed through the antenna switching circuit (D12), APC detector circuit (D14, D17, L39), and low-pass filter (L47, L48, C208, C210, C217) and is then applied to the antenna connector. Control voltage for the power amplifier (IC10, pin 2) are controlled by the APC circuit to protect the power module from a mismatched condition as well as to stabilize the output power. 4-2-3 DIGITAL SWITCH

CIRCUIT (LOGIC UNIT) The signals from the analog switch (IC6, pin 3) pass through the high-pass filter (IC3, pins 6, 7), and are then reapplied to the analog switch (IC6, pin 9). The signals are applied to the DI/AN switch via the "MOD_I" line. · IN CASE OF DIGITAL AF SIGNALS The AF signals are applied to the DI/AN switch (IC522, pin 10), and are then applied to the optional digital voice unit after being passed through the J4 (pin 4) via the "ANALOGUE_MOD_IN" line. The A/D converted AF signals are applied to the DI/AN switch (IC522, pin 1) from the J4 (pin 3) via the "DIGITAL_MOD_OUT" line. The digital AF signals from DI/AN switch (IC522, pin 2) are applied to the modulation circuit after being passed through the J3 via the "MODIN" line.

· ALC CIRCUIT "THROUGH" from the CPU (MAIN unit; IC1, pin 80) Through switch MIC switch Microphone IC2, D4 MIC IC3A IC520 ALC switch IC514 ALC IC520 IC520 ALC switch Switch IC525 to the modulator circuit "ALC" from the CPU (MAIN unit; IC1, pin 81) "THROUGH" from the CPU (MAIN unit; IC1, pin 80) Power supply Q524 4-3 4-2-7 APC CIRCUIT (MAIN UNIT) The APC circuit protects the power amplifier from a mismatched output load and stabilizes the output power. @@@@The PLL circuit compares the phase of the divided VCO frequency to the reference frequency. The PLL output frequency is controlled by the divided ratio (N-data) of a programmable divider. @@@@IC10 Power amp.

@@@@@FC SQLIN DTC MOD T2 Outputs reference frequency control voltage.

Outputs squelch control signal to adjust threshold level. Outputs DTCS slope correction signal. Outputs transmitting deviation control signal. Outputs RF tracking voltage while receiving.



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· Outputs RF tracking voltage while receiving.

@@@The voltage is converted from the +8V line at the regulator circuit. @@@High: While muting. Outputs R5 regulator (Q25) control signal. Low: While receiving. Outputs T8 regulator (Q14 and Q15) control signal. High: While transmitting. Outputs 2nd IF filter select signal ([W/N] version ONLY). @@@Outputs VCO regulator (Q1, Q3, Q5, Q8, D1) control signal. Low: While transmitting. @@Input port for squelch volume detecting signal.

Input port for the [] and [] switches from the external microphone. Input port for the S-meter voltage detecting signal. Input port for the PLL lock voltage. @@Input port for the weather channel alert detecting signal. Outputs LCD backlight color select signal. Low: Umber color is selected. @@Input port for the reset signal. Input port for [POWER] switch. Low: [POWER] switch is pushed. Input port for the noise pulse signal for squelch and CPU.

Pin number 5355 56, 57 5861 62 Port name IS0IS2 Description Output initial matrix strobe signals. KS1, KS2 Output key scan signals KR3KR0 PTT Input ports for the keyboard matrix signals. Input port for the [PTT] switch signal. High: While transmitting. Outputs digital/analog select signal while the UT-115 is installed.

High : Digital is selected. 64 DI_SEC 65 Outputs filter characteristic control signal while the UT-115 is installed. TX_DIGI Low : While transmitting digital signal. · Outputs serial data to D/A convertor (MAIN unit; IC5, pin 8). · Outputs DTMF clock signal to the expander IC (MAIN unit; IC2, pin 2).

Outputs clock signal to the optional unit. · Input port for the optional unit detecting signal. · Outputs strobe signal to the D/A converter (MAIN unit; IC5, pin 6). Input port for the optional unit detecting signal. 66 SO 8 9 10 14 19 25 26 28 29 30 31 34 35 40 COLOR DIM0 DIM1 CSHIFT RESET PWRSW NOIS DICK DIUD CLIN CLOUT SCK BUSY 67 OPSCK 68 OPV1 DASTB 69 OPV2 73 Input ports for encoder signals. Input port for the cloning signal. Output port for the cloning signal. Outputs serial clock signal to the D/A convertor (MAIN unit; IC5, pin 7). Outputs "BUSY" signal to the UT-115. 74 75 76 Input port for detecting signal whether the multifunction microphone is conEXTMIC nected or not.

Low: The multifunjtion microphone is connected. DUSE ESCK ESDA Outputs low-pass filter cut-off frequency control signal when using DTCS. Outputs clock signal to the EEPROM (IC5, pin 6). I/O port for data signals from/to the EEPROM (IC5, pin 5). Outputs the optional unit power supply controll signal. High : Power is ON. Outputs the ALC circuit control signal. High : The ALC circuit is OFF. Outputs the ALC circuit control signal. High : The ALC circuit is ON. I/O ports for the optional unit control signals. Output LCD segment signals. Output LCD common signals. Output LCD device signals. Input port for the current drain detecting signal while transmitting.

Outputs [POWER] switch control sigPWRON nal. High: [POWER] is ON. BSHIFT Outputs band shift signal. Low : While receiving Air (118136 MHz). band 77 OP_PS 42 80 81 84 85 86 90, 92127 128131 134136 144 THROUGH ALC OPT3 OPT2 OPT1 SEG1 SEG36 COM4 COM1 VL3 VLI IDET 43 44 PLLSTB PLLCK Outputs strobe signal to the PLL IC (MAIN unit; IC1, pin 4).

Outputs clock signal to the PLL IC (MAIN unit; IC1, pin 2). · Outputs strobe signal to the expander IC (MAIN unit; IC2, pin 1). · Input port for the optional unit detecting signal. Input port for the PLL unlock signal. Input port for the HM-133V remote microphone signals. Output transmitting/receiving AF filter select signal to the analog swtich (IC6, pin 6 ["AFON" is applied to the pin 12]). 4-6 45 EXSTB OPV3 UNLK REMO 46 47 49 51 MICS AFON 4-6 UT-115 CIRCUIT DESCRIPTION 4-6-1 RECEIVER CIRCUIT The detected digital signals "FMDET" from the connected transceiver via the J301 (pin 22) are amplified at the buffer amplifier (IC251, pin 2). The amplified signals are applied to the GMSK modem circuit (IC252, pin 11), and are then applied to the CPU (IC204) as clock synchronizer digital signal. The digital signals from the CPU are applied to the AMBE voice CODEC IC (IC151) to precess code extention, and are then applied to the linear CODEC IC (IC50) as 32 bits digital voice data. The applied digital signals are converted to the analog AF signals at the D/A converter section (IC50), and are then applied to the connected transceiver via the J301 (pin 21) as "DAFOUT" signal.

4-7 UT-115 POWER SUPPLY CIRCUITS VOLTAGE LINES Line Description The 5 V voltage from the connected transceiver via the J301 (pin 29). The 5V line is controlled by the +5V control circuit (Q50 and Q51). @@Common 3.3 V converted from the 5V line by the 3.3V regulator circuit (IC1). One of the 3.3 V line is controlled by the +3V control circuit (Q400 and Q401). @@@@Outputs the PLL band width control signal while receiving. Outputs transmitting data signal to the CPU (IC204, pin 54). Input port for receiving data signal from the CPU (IC204, pin 53).

Input port for receive clock signal from the CPU (IC204, pin 52). @@@@Q301, Q302 and Q303 convert control signals level between the UT-115 and the connected transceiver. · UT-115 BLOCK DIAGRAM - "TXD_2" signal to the J301, pin 16 - "RXD_2" signal to the J301, pin 17 - "232C_RX" signal to the J301, pin 25 - "232C_TX" signal to the J301, pin 26 "DAFOUT" signal to the J301, pin 22 Level converter Q305, Q306 CPU CODEC IC204 DSP CODEC Modem IC252 Buffer IC253B "DMOD" signal to the J301, pin 3 RS-232C IC351 Buffer IC151 IC251A "FMDET" signal from the J301, pin 23 Linear CODEC Buffer IC50 IC251B "AMODIN" signal from the J301, pin 4 4-7 4-8-2 CPU (IC2) Pin number 7 10 11 16 Port name RES OSC2 OSC1 APWR Description Input port for the reset signal from the reset IC (IC203, pin 1). Low :The CPU is reset. Outputs 9.

8304 MHz reference signal. Input port for 9.8304 MHz reference signal. Outputs 3.2V regulator circuit (IC2, pin 3) control signal.

29 Outputs receive AF mute control signal to the connected transceiver via the RMUTE_C J301 (pin 24). High :While muting. Outputs PTT signal to the connected PTTOUT_C transceiver via the J301 (pin 2). High :While transmitting. ATXD ACLK ASTB ARXD ARES Outputs transmit data signal to the CPU (IC151, pin 32). Outputs clock signal to the CPU (IC151, pins 28 and 34). Outputs strobe signal to the CPU (IC151, pins 30 and 38). Input port for receive data signal from the CPU (IC151, pin 42). Outputs reset signal to the linear CODEC IC (IC50, pin 13). Output power save control signal to the +3V controller circuit (Q400 and Q401).

Low :Power save is ON. Input port for the BUSY signal from the connected transceiver via the J301 (pin 6). High :While receiving signals.



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Input port for the PTT signal from the connected transceiver via the J301 (pin 1). High :While transmitting, 31 33 34 36 37 38 58 59 PSAVE 61 BUSY 62
PTTN 4-8 SECTION 5 ADJUSTMENT PROCEDURES 5-1 PREPARATION Enter the adjustment mode when adjusting the IC-2200H Then the JIG cable as
shown the page 5-2 is required . I REQUIRED TEST EQUIPMENT EQUIPMENT DC power supply GREDE AND RANGE Output voltage Current capacity
Measuring range Frequency range Impedance SWR Frequency accuracy Sensitivity Frequency range Output level : 13.8 V DC : 20 A or
more : : : 0.1100 W 50400 MHz 50 Less than 1.2 : 1 EQUIPMENT FM deviation meter Oscilloscope Audio generator Attenuator GREDE AND RENG
Frequency range Measuring range Frequency range Measuring range Frequency range Measuring range Power attenuation Capacity : 0400 MHz : 0 to ± 5
kHz : DC400 MHz : 0.

0110 V : 3003000 Hz : 1500 mV : 50 or 60 dB : 150 W or more RF power meter (terminated type) Frequency counter : 0.1400 MHz : ± 1 ppm or better : 100
mV or better : 0.1400 MHz : 0.1 μ V32 mV (127 to 17 dBm) Standard signal generator (SSG) I ENTERING THE ADJUSTMENT MODE q Turn the
transceiver's power OFF. w Connect the JIG cable to the [MIC] jack.

e Push and hold the [SET] and [MONI] keys, and then turn power ON. NOTE: Exiting from the adjustment mode when the transceiver's power is OFF. I
OPERATING ON THE ADJUSTMENT MODE Change the adjustment value : [DIAL] Verifying the adjustment value : [M/CALL] key Forward the adjustment
item : [BANK] key Go back the adjustment item : [V/MHz] key Store the adjustment value in the memory : [S.MW] key CAUTION: Need to push the [S.MW]
key when storing the adjustment value in the memory.

Otherwise, the transceiver is not adjusted properly. IMPORTANT!: The transceiver need to be cancelled adjustment mode (as following method) to use
normal mode when adjustments are finished, otherwise the transceiver does not work properly. q: Turn the power OFF. w: Push and hold [S.MW] and [SET]
keys, and then turn the power ON. 5-1 CONNECTIONS FM DEVIATION METER ATTENUATOR 50 dB or 60 dB RF POWER METER 50 , 1 80 W
STANDARD SIGNAL GENERATOR 0.1 300 MHz 127 to 17 dBm (0.1 V to 32 mV) to the antenna connector POWER SUPPLY 13.8 V, 20 A or more

CAUTION! DO NOT transmit while an SSG is connected to the antenna connector. JIG CABLE INFORMATION AUDIO GENERATOR 300 Hz to 3 kHz pin
5 (MICE) pin 6 (MIC) pin 7 (GND) pin 4 (PTT) *dP tr *F4 *dE+[DIGITAL] dt+[DIGITAL] *F5 *FA *F6 *F9 NOTE: DO NOT adjust items with "*"
mark at below explanation.

Those items are adjusted automatically when other adjustment items are adjusted. to the microphone connector ADJUSTMENT ITEM EXPLANATION
Starting item Fr rE *F1 *F2 PO *dt *F3 *C6 *F4 *db PO+[MID] *CS *F5 *db *Et *F6 *db PO+[MID]+[LOW] *dF *F7 *db Sr+[.] *F8 *dd *F9 *dd
PO+[LOW] Sr Sq *F0 *dd tr *F1 *dd PU tr *F2 *dE dE tr *F3 *dE dt *bP *F7 *F8 *dE+[DIGITAL] *dE+[DIGITAL] *db+[DIGITAL] *PN *PP
*dE+[DIGITAL] *db+[DIGITAL] *51 *52 *53 *dd+[DIGITAL] *db+[DIGITAL] *54 *55 *56 *dd+[DIGITAL] *db+[DIGITAL] *57 *dU *51+[.] *FN
*dd+[DIGITAL] *db+[DIGITAL] *52+[.] *FN *FN *53+[.] *FN *dd+[DIGITAL] *dd+[DIGITAL] *54+[.] *57+[.] *55+[.] *56+[.] *Not adjust items 5-2
5-2 SOFTWARE ADJUSTMENTS (TRANSMITTING AND RECEIVING) ADJUSTMENT ADJUSTMENT CONDITION OPERATION · Turn the [DIAL] to set
to 146.

0000 MHz : 146.0 MHz REFERENCE 1 · Operating frequency · Loosely couple a frequency counter to the antenna · Push the [S.MW] key. FREQUENCY
connector on the rear panel.

[Fr] · Transmitting REFERENCE 1 · Operating frequency · Receiving VOLTAGE [rE] VHF OUTPUT POWER (High) [PO] (Middle) [PO+MID] : 146.0
MHz · Push the [S.MW] key. · Turn the [DIAL] to set the VHF high power to 67 W. @@ · Push the [S.

MW] key. @@ · Push the [S.MW] key. · Turn the [DIAL] to set the VHF low power to 10 W. · Push the [S.MW] key. · Turn the [DIAL] to set the VHF low
power to 5 W. @@@@ · Transmitting : 146.0 MHz · Turn the [DIAL] to set to ± 4.6 kHz.

FREQUENCY 1 · Operating frequency · IF bandwidth : Wide · Push the [S.MW] key after finish adjustment. DEVIATION · Connect an audio generator to the
[MIC] jack and set (Analog) as : 1 kHz/80 mV rms [dE] · Connect an FM deviation meter to the antenna connector and set as : HPF : OFF LPF : 20 kHz De-
emphasis : OFF Detector : (PP)/2 · Transmitting : 146.0 MHz · Turn the [DIAL] to set to ± 1.25 kHz. DIGITAL VCO 1 · Operating frequency · IF bandwidth :
Wide · Push the [S.MW] key after finish adjustment. DEVIATION · No audio signal is applied to the [MIC] jack. @@@@ · IF bandwidth : Wide
REFERENCE · No audio signal is applied to the [MIC] jack. DEVIATION · Connect an FM deviation meter to the antenna con[dt+DIGITAL] nector and set
as : HPF : OFF LPF : 20 kHz De-emphasis : OFF Detector : (PP)/2 · Transmitting · Push the [S.

MW] key. : 118.020 MHz 1 · Operating frequency RECEIVER · Connect an SSG to the antenna connector and set as SENSITIVITY Level : 0.5 μ V* (113 dBm)
[tr] Modulation : 1 kHz Deviation : ± 3.5 kHz · Receiving 2 · Operating frequency · Receiving 3 · Operating frequency · Receiving 4 · Operating frequency ·
Receiving SQUELCH LEVEL (Wide) [Sq] : 136.

020 MHz : 147.980 MHz : 173.980 MHz · Push the [S.MW] key. · Push the [S.

MW] key. · Push the [S.MW] key. · Turn the [DIAL] to set to squelch tight point. : 145.020 MHz 1 · Operating frequency · Connect an SSG to the antenna
connector and set as · Turn the [DIAL] to set to squelch threshold point. · Push the [S.MW] key. Level : 0.1 μ V* (127 dBm) Modulation : 1 kHz Deviation : \pm
3.

5 kHz · Receiving · Push the [S.MW] key. : 145.020 MHz 1 · Operating frequency · Connect an SSG to the antenna connector and set as Level : 1.3 μ V* (105
dBm) Modulation : 1 kHz Deviation : ± 3.5 kHz · Receiving · Push the [S.MW] key. : 127.020 MHz 2 · Operating frequency · Connect an SSG to the antenna
connector and set as Level : 1.0 μ V* (107 dBm) Modulation : 1 kHz Deviation : 30 % · Receiving S-METER (FM mode) [Sr] (AM mode) [Sr+.

] *This output level of a standard signal generator (SSG) is indicated as SSG's open circuit. 5-4 5-3 UT-115 ADJUSTMENT PROCEDURES ·
PREPARATION Enter the adjustment mode when adjusting the UT-115. I REQUIRED TEST EQUIPMENT EQUIPMENT DC power supply 1 DC power
supply 2 GREDE AND RANGE Output voltage Current capacity Output voltage Current capacity : 5.



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0 VDC : 1 A or more : 8.0 VDC : 1 A or more EQUIPMENT Frequency counter GREDE AND RENGE Frequency range Frequency accuracy Sensitivity
Frequency range Measuring range : 0.

1100 MHz : ±1 ppm or better : 100 mV or better : DC400 MHz : 0.0110 V Oscilloscope I ENTERING THE ADJUSTMENT MODE Connect J301, pin 14 to GND, then the UT-115 enters adjustment mode. NOTE: Exit from the adjustment mode to disconnect J310, pin 14 from GND before the UT-115 adjustment is finished I CONNECTION Downloaded by RadioAmateur.EU · UT-115 BOTTOM VIEW Power supply 5 V DC / 1 A Power supply 8 V DC / 1 A J301, Pin 14 Entering adjustment mode 5-5 5-4 TRIMMER ADJUSTMENTS ADJUSTMENT 1 CODEC FREQUENCY SET 1 MODEM FREQUENCY SET MODURATION 1 OUTPUT LEVEL 2 3 MEASUREMENT UNIT MAIN MEASUREMENT LOCATION Connect the frequency counter to the check point CP101. Connect the frequency counter to the check point CP216.

· Transmit the UT-115 to connect J301, pin 1 to GND. · Connect the oscilloscope to the J301, pin 21. Disconnect J301, pin 1 from GND. VALUE 16.38400 MHz ±10 Hz ADJUSTMENT POINT UNIT MAIN ADJUST C102 MAIN 2.457600 MHz ±3 Hz MAIN C207 MAIN 350 mVp-p ±10 mV MAIN R264 · UT-115 TOP VIEW C207 MODEM frequency set adjustment C102 CODEC frequency set adjustment · UT-115 BOTTOM VIEW CP216 MODEM frequency set check point J301, Pin 1 Transmit the UT-115 J301, Pin 21 Modulation output level check point CP101 CODEC frequency set check point 5-6 SECTION 6 6-1 IC-2200H [LOGIC UNIT] REF NO. IC1 PARTS LIST [LOGIC UNIT] REF NO. DESCRIPTION M. B B B B B B T T B T B B B T R16 R17 R18 R19 R20 R21 R22 R23 R24 R25 R26 R27 ORDER NO. 1140011860 1140011861 1140011910 1130004200 1110005340 1110006210 1140008650 1130011770 1110002860 1110005310 1130011770 1130008560 1130011770 1130011780 1130011770 S.

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7030007350 7030005090 7030005050 7030007340 7030005720 7030007570 7030005070 7030005090 7030008010 7030005090 7030008400 7030005100
7030005240 7030008010 7030005160 7030005160 7030005160 7030008410 7410000770 7030005090 7030005160 7030005600 7030005160 7030005530
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7030005100 7030007350 7030005050 7030005110 7030008300 7030005100 7030005070 7030008290 7030009290 7030005090 7030005040 7030005120
7030005310 7030005120 7030005590 7030005030 7030005210 7030009160 7410000950 7030009280 7030005310 7030008290 7030005120 7030005570
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RESISTOR DESCRIPTION ERJ2GEJ 683 X (68 k) ERJ2GEJ 683 X (68 k) ERJ2GEJ 563 X (56 k) ERJ2GEJ 102 X (1 k) ERJ2GEJ 102 X (1 k) ERJ2GEJ 683
X (68 k) ERJ2GEJ 102 X (1 k) ERJ2GEJ 104 X (100 k) ERJ2GEJ 153 X (15 k) ERJ2GEJ 103 X (10 k) ERJ2GEJ 223 X (22 k) ERJ3GEYJ 393 V (39 k) [EXP]
only ERJ2GEJ 393 X (39 k) [other] ERJ2GEJ 104 X (100 k) ERJ2GEJ 103 X (10 k) ERJ2GEJ 153 X (15 k) ERJ2GEJ 563 X (56 k) ERJ2GEJ 122X (1.2 k)
ERJ2GEJ 683 X (68 k) ERJ2GEJ 104 X (100 k) ERJ2GEJ 123 X (12 k) ERJ2GEJ 104 X (100 k) ERJ2GEJ 182 X (1.8 k) ERJ2GEJ 154 X (150 k) ERJ2GEJ
473 X (47 k) ERJ2GEJ 123 X (12 k) ERJ2GEJ 105 X (1 M) ERJ2GEJ 105 X (1 M) ERJ2GEJ 105 X (1 M) ERJ2GEJ 392 X (3.9 k) EXB-V4V 102JV (1 k)
ERJ2GEJ 104 X (100 k) ERJ2GEJ 105 X (1 M) ERJ2GEJ 273 X (27 k) ERJ2GEJ 105 X (1 M) ERJ2GEJ 100 X (10) ERJ2GEJ 102 X (1 k) ERJ2GEJ 155 X
(1.5 M) ERJ2GEJ 102 X (1 k) [EUR], [EXP], [EXP-1] only ERJ3GEYJ 1R0 V (1) ERJ2GEJ 153 X (15 k) ERJ2GEJ 104 X (100 k) ERJ2GEJ 153 X (15 k)
ERJ2GEJ 563 X (56 k) EVU-F2AF20 A14 (10KA) EVU-F2AF20 B14 (10KB) ERJ2GEJ 105 X (1 M) ERJ2GEJ 183 X (18 k) ERJ2GEJ 154 X (150 k)
ERJ2GEJ 393 X (39 k) ERJ2GEJ 103 X (10 k) ERJ2GEJ 224 X (220 k) ERJ2GEJ 184 X (180 k) ERJ2GEJ 154 X (150 k) ERJ2GEJ 683 X (68 k) ERJ2GEJ
183 X (18 k) ERJ2GEJ 562 X (5.6 k) ERJ2GEJ 104 X (100 k) ERJ2GEJ 472 X (4.7 k) ERJ2GEJ 102 X (1 k) ERJ2GEJ 124 X (120 k) ERJ2GEJ 102 X (1 k)
ERJ2GEJ 680 X (68) ERJ2GEJ 152 X (1.5 k) ERJ2GEJ 822 X (8.2 k) ERJ2GEJ 181 X (180) EXB-V8V 102JV ERJ2GEJ 124 X (120 k) ERJ2GEJ
183 X (18 k) ERJ2GEJ 102 X (1 k) ERJ2GEJ 820 X (82) ERJ2GEJ 393 X (39 k) ERJ2GEJ 151 X (150) ERJ2GEJ 331 X (330) ERJ2GEJ 472 X (4.7 k)
ERJ2GEJ 154 X (150 k) M.

B T T B B B B T B
B B B IC2 IC3 IC4 IC5 IC6 IC24 IC514 IC520 IC521 IC522 IC524 IC525 M30220FCRP (FX-2698A) [EXP] M30220FCRP (FX-2698A-1) [USA]
M30220MA-132RP (FX-2698A-1) except [EXP] and [USA] TC4S66F (TE85R) NJM12902V-TE1 BD5242FVE-TR HN58X2464TI CD4066BPWR TA75S393F
(TE85R) AN6123MS CD4066BPWR TC75S51F (TE85L) CD4066BPWR SN74AHC2G53HDCTR CD4066BPWR Q1 Q2 Q3 Q5 Q6 Q7 Q8 Q12 Q513 Q514
Q515 Q524 Q525 Q527 Q528 1530002690 1590000430 1590001650 1590000660 1510000510 1530002690 1530002690 1530002690 1520000200
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D17 D21 D22 D23 D25 D27 D28 D526 1730002280 1790001250 1730002280 1730002280 1750000520 1750000370 1750000520 1750000520 1750000520 1710000611
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DAN222TL ISS133 T77 (26M/M) [TPE], [KOR], [EUR-1] only ISS400 TE61 except [EXP], [EXP-1] ISS400 TE61 [EXP], [EXP-1] only ISS400 TE61 [EUR], [EUR-1], [EXP], [EXP-1] only ISS400 TE61 [TPE], [KOR], [EXP], [EXP-1] only DAN222TL ISS400 TE61 [TPE], [EXP], [EXP-1] only ISS400 TE61 [TPE], [KOR] only MA2S077-(TX) ISS400 TE61 except [EXP-1] ISS400 TE61 [EXP], [EXP-1] only MA2S111-(TX) T T B B B T T T B B B B T B B B B B B R28 R29 R30 R31 R32 R34 R35 R36 R37 R38 R39 R40 R41 R42 R43 R44 R45 R46 R47 R48 R49 R50 R51 R52 R53 R55 R56 R57 R58 R59 R60 R61 R62 R64 R67 R69 R70 R71 R72 R73 R74 R75 R76 R77 R78 R79 R80 R81 R82 R83 R84 R85 R86 R87 R89 R91 R92 R93 R94 R96 R97 R100 R101 R103 XI 6050011500 S.



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XTAL CR-739 (9.8304 MHz) B R1 R2 R3 R4 R5 R6 R7 R8 R9 R10 R11 R12 R13 R14 R15 7030008010 7030005310 7030005050 7030005050 7030005230
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(330 k) (100 k) (100) (2.2 k) (22 k) (10 k) (100 k) (100 k) (12 k) (10 k) (1 k) T T T B T T B T T T B T B B M.=Mounted side (T: Mounted on the Top side, B:
Mounted on the Bottom side) S.=Surface mount 6-1 [LOGIC UNIT] REF NO. R104 [LOGIC UNIT] DESCRIPTION S.RESISTOR S.RESISTOR S.RESISTOR
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JPW ERJ2GEJ 103 X (10 k) ORDER NO. 7030005080 7030008290 7030005090 7030005050 7030005090 7030005050 7030005600 7030008010
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7030010040 7030005050 M. B B B B B B B B B B B B B B B T B B B B B B B B T T B B T T T T B B T T T B B REF NO. C53 C54 C55 C56
C57 C59 C62 C64 C65 C66 C67 C68 C69 C70 C71 C72 C73 C74 C75 C76 C77 C78 C79 C80 C81 C82 C583 C584 C585 C586 C587 C588 C591 C592
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ECEV1CA100SR ECJ0EB1A104K ECJ0EB1A104K ECJ0EB1C103K ECJ0EB1C103K ECJ0EB1A333K ECEV1HA2R2SR ECJ0EB1E562K ECJ0EB1E271K
ECJ0EB1A333K ECEV1HA2R2SR ECJ0EB1C103K ECJ0EC1H221J ECJ0EB1C103K ECJ0EB1C103K ECJ0EB1E471K ECJ0EB1A104K ECJ0EB1A104K
ECJ0EB1A104K ECEV1CS100SR TEESVA 1A 106M8L C1608 JB 1A 105K-T ECJ0EB1E102K ECJ0EB1E102K ECJ0EB1A104K ECJ0EB1E102K
ECJ0EB1E102K ECJ0EB1E102K ECJ0EB1E102K ECJ0EB1A104K ECJ0EB1E102K ECST1CX106R TEESVA 1A 106M8L M. B B B B B B B B T B B T
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R550 R551 J1 J3 J4 6450002210 6510024230 6510021970 CONNECTOR S.

CONNECTOR S.CONNECTOR 3017-8821 <KIN> 08-6210-0363-40-800 AXN330C130P T B B C1 C4 C5 C7 C8 C9 C10 C11 C13 C14 C15 C16 C19 C20
C23 C28 C30 C31 C32 C33 C34 C35 C36 C37 C38 C39 C40 C41 C42 C43 C44 C45 C46 C47 C48 C49 C51 C52 4030017460 4030017460 4030017420
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ECJ0EB1E102K ECJ0EB1E102K ECJ0EC1H470J ECJ0EC1H470J ECEV1CA100SR C1005 JB 1C 473K-T-N ECJ0EB1E682K ECJ0EB1A333K
ECJ0EB1A104K ECJ0EB1C103K ECJ0EB1H182K ECJ0EB1E102K ECJ0EB1E102K ECJ0EB1A104K ECJ0EB1A473K ECJ0EB1A473K ECJ0EB1E562K
ECJ0EB1C103K ECJ0EB1C223K ECJ0EB1A104K ECJ0EB1H222K ECJ0EB1A104K ECJ0EB1A273K ECJ0EC1H470J B T T B T B B B B B B B B T B B
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EP4 EP5 EP6 EP7 EP8 EP9 EP10 EP11 6910012350 6910012350 6910012350 6910012350 6910012350 6910012350 6910012350 6910012350 6910012350 6910012350
6910012350 6910012350 8930062251 S.BEAD S.BEAD S.BEAD S.BEAD S.
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BEAD LCD CONTACT MMZ1608Y 102BT MMZ1608Y 102BT MMZ1608Y 102BT MMZ1608Y 102BT MMZ1608Y 102BT MMZ1608Y 102BT MMZ1608Y 102BT MMZ1608Y 102BT MMZ1608Y
102BT MMZ1608Y 102BT MMZ1608Y 102BT MMZ1608Y 102BT SRCN-2698-SP-N-W-1 T B B T B B B B B T M.=Mounted side (T: Mounted on the Top
side, B: Mounted on the Bottom side) S.=Surface mount 6-2 [LOGIC UNIT] REF NO. EP12 [MAIN UNIT] DESCRIPTION PCB B 6048B ORDER NO.
0910057732 M. REF NO. D30 D31 D38 ORDER NO. 1750000550 1790001250 1720000730 S.DIODE S.DIODE S.

VARICAP DESCRIPTION ISS355 TE-17 MA2S111-(TX) MA2S30400L M. B B T F11 F12 2020001520 2020001460 2030000270 2030000270 CERAMIC
CERAMIC MONOLITH MONOLITH [MAIN UNIT] REF NO. IC1 IC2 IC4 IC5 IC6 IC7 IC8 IC9 IC10 IC11 IC12 ORDER NO. 1130008350 1130011760
1110003490 1190000350 1110004050 1180001250 1180000970 1110002550 1150002180 1110004050 1130011780 S.IC S.IC S.IC S.IC S.IC S.IC S.
IC IC S.IC S.IC S.IC DESCRIPTION LV2105V-TLM CD4094BPWR TA31136FN (D,EL) M62363FP-650C NJM3404AV-TE1 TA7808F (TE16L)
AN78L05M-(E1) TA7252AP S-AV36 (J) NJM3404AV-TE1 SN74AHC2G53HDCR M. B T B B B B B T T T T F13 F14 CFWLA450KFFA-B0 (CFWS450F)
CFWLA450KHFA-B0 (CFWS450HT) except [EXP-1] FL-363 (21.

7 MHz) FL-363 (21.7 MHz) T T T T X1 X2 6050011870 6070000200 S.XTAL CR-779 (21.250 MHz) DISCRIMINATOR CDBLA450KCAY24-B0
(CDB450C24) T T Q1 Q2 Q3 Q4 Q5 Q6 Q7 Q8 Q9 Q10 Q11 Q12 Q13 Q14 Q15 Q16 Q17 Q19 Q21 Q22 Q25 Q26 Q27 Q28 Q29 Q30 Q31 Q34 Q35 Q36
Q37 Q38 Q39 Q40 Q41 1530002850 1590001650 1530002850 1530002850 1530002850 1530002850 1530002920 1530002920 1590001040 1530002920 1560000810
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DTA144EUA T106 except [EXP-1] 2SA1362-Y (TE85L) 2SC4116-BL (TE85R) 2SC4406-4-TL 2SC3356-T1B 3SK299-T1 U73 DTB113ZK T146 DTC144EUA
T106 DTB113ZK T146 DTA144EUA T106 3SK299-T1 U73 2SC4684 (TE16R) 2SC4213-B (TE85R) DTA143TU T106 DTC143ZUA T106 2SC4116-BL
(TE85R) 2SC2954-T1 DTA113ZU T106 XP6501-(TX) .AB XP6501-(TX) .AB DTC144EUA T106 DTC144EUA T106 2SC4617 TLS B B B B B T T T T T T
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L43 L45 L46 L47 L48 L52 L56 L57 L58 L59 L60 L61 L62 6200009350 6200003640 6200001620 6200009920 6200010570 6200004870 6200004880
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D3 D4 D5 D6 D7 D9 D10 D11 D12 D13 D14 D15 D16 D17 D18 D19 D20 D21 D23 D24 D25 D26 D27 D29 1790001250 1750000720 1750000770
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XB15A407 HVC350BTRF MA742 (TX) ISS355 TE-17 HVU131TRF MA742 (TX) MA2S111-(TX) XB15A308 RD20E B2 DSA3A1 MA2S111-(TX)
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RESISTOR ERJ3GEYJ 683 V (68 k) ERJ3GEYJ 102 V (1 k) ERJ3GEYJ 474 V (470 k) ERJ3GEYJ 470 V (47) [EXP] ERJ3GEYJ 820 V (82) [other]
ERJ3GEYJ 472 V (4.7 k) ERJ3GEYJ 471 V (470) NTCG16 4LH 473KT ERJ3GEYJ 104 V (100 k) ERJ3GEYJ 103 V (10 k) ERJ3GEYJ 473 V (47 k)
ERJ3GEYJ 103 V (10 k) ERJ3GEYJ 101 V (100) ERJ3GEYJ 272 V (2.7 k) ERJ3GEYJ 105 V (1 M) ERJ3GEYJ 274 V (270 k) ERJ3GEYJ 223 V (22 k)
ERJ3GEYJ 224 V (220 k) ERJ3GEYJ 391 V (390) ERJ3GEYJ 822 V (8.2 k) ERJ3GEYJ 562 V (5.6 k) ERJ3GEYJ 101 V (100) ERJ3GEYJ 102 V (1 k)
ERJ3GEYJ 223 V (22 k) ERJ3GEYJ 222 V (2.



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ERJ3GEYJ 334 V (330 k) ERJ3GEYJ 564 V (560 k) ERJ3GEYJ 473 V (47 k) ERJ3GE JPW V [EXP-1] only ERJ3GE JPW V [EXP-1] only ERJ3GE JPW V PTFM04BC222Q2N34B0 ERJ3GE JPW V ERJ3GEYJ 473 V (47 k) ERJ3GE JPW V ERJ3GEYJ 104 V (100 k) ERJ3GE JPW V ERJ8GEYJ JPW ERJ3GE JPW V ERJ3GEYJ 473 V (47 k) ERJ3GEYJ 473 V (47 k) ERJ3GEYJ ERJ3GEYJ ERJ3GEYJ ERJ3GEYJ ERJ3GEYJ ERJ3GEYJ ERJ3GEYJ ERJ3GEYJ ERJ3GEYJ ERJ3GEYJ 121 332 473 473 271 180 271 102 V V V V V V V V M.



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